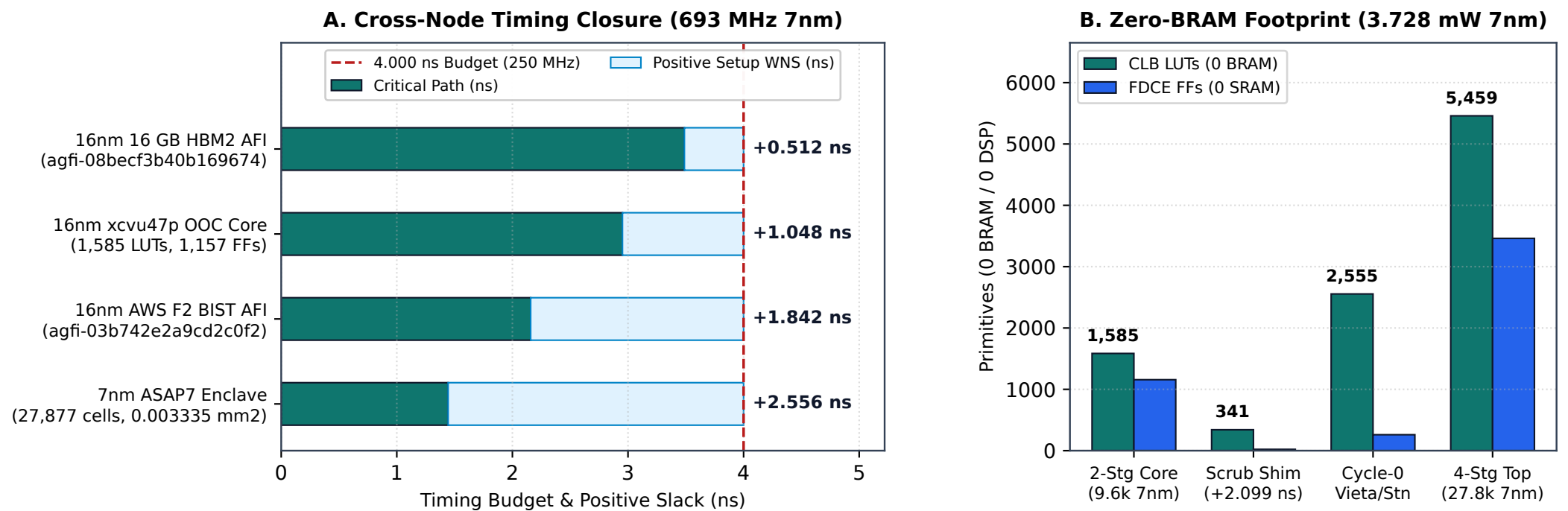

A. Cross-Node Timing Closure (693 MHz 7nm)
4.000 ns Budget (250 MHz)
Positive Setup WNS (ns)
Critical Path (ns)
16nm 16 GB HBM2 AFI
(agfi-08becf3b40b169674)
+0.512 ns
16nm xcvu47p OOC Core
(1,585 LUTs, 1,157 FFs)
+1.048 ns
16nm AWS F2 BIST AFI
(agfi-03b742e2a9cd2c0f2)
+1.842 ns
7nm ASAP7 Enclave
(27,877 cells, 0.003335 mm2)
+2.556 ns
0
1
2
3
4
5
Timing Budget & Positive Slack (ns)
B. Zero-BRAM Footprint (3.728 mW 7nm)
CLB LUTs (0 BRAM)
FDCE FFs (0 SRAM)
Primitives (0 BRAM / 0 DSP)
6000
5000
4000
3000
2000
1000
0
1,585
341
2,555
5,459
2-Stg Core
(9.6k 7nm)
Scrub Shim
(+2.099 ns)
Cycle-0
Vieta/Stn
4-Stg Top
(27.8k 7nm)